

**PATENT NUMBER**

## U.S. UTILITY Patent Application

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FA SCANNED	332	
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APPLICATION NO. 09/893455	CONT/PRIOR D F	CLASS <del>425</del> 264	SUBCLASS 40.3	ART UNIT <del>1722</del> 1732	EXAMINER ORT72
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**TITLE**      **APPLICANTS**

Hiroyuki Nishi  
Akira Sugai

Transfer molding apparatus and method for manufacturing semiconductor devices

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  <div style="border: 1px solid black; height: 100px; width: 100%;"></div>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)			
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE FEE</b> <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">Amount Due</div> <div style="width: 45%;">Date Paid</div> </div>	
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